

# EE112 - Fall 2016

## Analog Integrated Circuits I

Prof. Haoyu Wang  
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5210 Research Bldg.

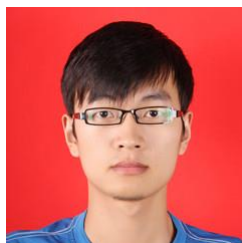
## Course Information

- Lecture
  - » 10:15-11:55am, Tuesday, Thursday
  - » Rm 1D-108, Bldg. SIST
- Instructor
  - » Prof. Haoyu Wang
  - » 5210 Research Bldg, [wanghy@shanghaitech.edu.cn](mailto:wanghy@shanghaitech.edu.cn)
  - » Office hours: Friday 1:00-3:00pm, the other times are fine, but by appointment only
- Recitation:
  - » Fridays 3:00-4:00pm

# Teaching Assistants



Recitation TA  
Zhiqing Li (李志清)  
lizhq@shanghaitech

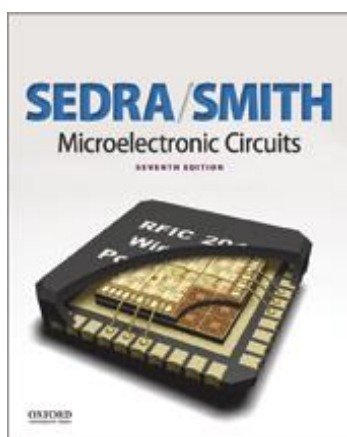


Lab TA 1  
Ming Shang (尚明)  
shangming@shanghaitech



Lab TA 2  
Liang Yu (于亮)  
yuliang@shanghaitech

# Textbook



- Sedra/Smith, Microelectronic Circuits, 7<sup>th</sup> edition
  - » Oxford University Press
- Minimum reading
  - » Assigned sections in the website
- Best to read the relevant sections before lecture
  - » Enables meaningful in class discussions

# Course Websites



- Open website:
  - » General course info, lecture notes, labs, hw problems
  - » <http://pearl.shanghaitech.edu.cn/ee112/>
- Piazza
  - » Discussions
  - » <https://piazza.com/shanghaitech.edu.cn/fall2016/ee112>
- Reference
  - » EE105 @ UC Berkley
  - » <http://www-inst.eecs.berkeley.edu/~ee105/sp16/>

# Homework



- Posted in class
- Due one week after it is posted
  - » Will be collected right **before** the lecture
  - » Late homework will be discounted by 20% per day
  - » Solution will be discussed in the recitation session
- Be prepared to spend 6-10 hours to complete
  - » Reading, problem solving
- You may discuss homework problems with other students in the class, the TAs, or the instructor.
- **The work you submit for grading must be your own**

# Labs (SIST 1A-103)



- Lab is an integral part of this course
- **You must complete all labs to pass the course!**
- 3 hours lab sessions
  - » Plenty of time if you do your Prelab in advance
  - » Not enough time if you are trying to figure out what to do on the spot
  - » Allow 5 to 10 hours for your Prelab. You may need to read ahead
- **Prelab** is due at the beginning of your lab session
  - » TA will check off your prelab
- **Lab reports** are due at the beginning of the following Lab
  - » Late report will be discounted by 20% per day
- Work in groups of 2 (find your partner now)
- Each student must individually turn in his/her own Prelab and Lab reports

# Grades



- Homework: 10%
  - » Lowest score will be dropped from grade calculation
- Lab 25%
  - » You must complete all labs to pass the course!
- Midterms: 15% x 2 = 30%
- Final exam: 30%
- Attendance(quizzes): 5%
  - » Quizzes might happen during the recitation section
- Cheating will result in automatic Fail!

# Midterm & Final Exams



- Midterm 1:
  - » 10/27/2016 (Thurs.) in class
- Midterm 2:
  - » 12/1/2016 (Thurs.) in class
- Final Exam:
  - » Exam week TBD

# Software



- LabVIEW from National Instruments



- SPICE (Simulation Program with Integrated Circuit Emphasis)
  - » Multisim from National Instruments
  - » PSPICE from Cadence
  - » LTSPICE
    - Free download from Linear Technology
    - <http://www.linear.com/designtools/software/#LTspice>

