

# A New High Gain DC-DC Converter With Model-Predictive-Control Based MPPT Technique for Photovoltaic Systems

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**Abstract**—High step-up DC/DC converter and maximum power point tracking (MPPT) control are essential components in photovoltaic (PV) systems. For the purpose of utilizing PV module in power generation, this manuscript proposes 1-new high step-up DC/DC converter 2-model predictive control based maximum power point tracking (MPC-MPPT) algorithm with optimum number of sensors. The suggested topology is able to provide a voltage gain up to 10 times of input-voltage and its measured efficiency is around 93%. MPC is a prevalent control technique with superior transient and steady-state performances in PV systems. However, in PV DC/DC converters, conventional MPC based MPPT technique typically requires two voltage-sensors and one current-sensor. For the purpose of reducing system cost, this manuscript presents MPC based MPPT technique with two-sensors. The algorithm is designed to operate with both fixed and adaptive step-size. Different scenarios and operating conditions are evaluated both in Simulink and with hardware set-up. Results obtained from the simulation and experimental work are consistent and verify the analytical analysis of the system.

**Index Terms**—DC/DC converter, model predictive control based maximum power point tracking (MPC-MPPT).

## I. INTRODUCTION

IN photovoltaic (PV) generation systems, a DC/DC converter is required to link the low voltage PV panel and the high voltage DC link. In this DC/DC converter, maximum-power-point-tracking (MPPT) control is required to fully exploit the capacity of the PV panel [1]–[2]. In general, high tracking accuracy, and stable transient and steady-state response are important criteria to evaluate the performance of MPPT techniques. In order to achieve such criteria, different MPPT techniques have been investigated in literature [3]–[6].

P&O algorithm is a classical algorithm that detects the maximum power of the PV panel from the slope of the PV curve.

But the output of the P&O algorithm suffers from oscillation around the maximum power point (MPP) [7], [8]. In [7], the controller speed is optimized during transient by an adaptive scaling factor. In [9], a fixed scaling factor is used to change the perturbation step during the transient. InCond method is a step towards solving the problem of P&O algorithm. It eliminates the oscillation around the MPP and leads to more efficiency. Authors in [10], have developed MPPT technique called delta P&O and a variable step size is suggested to improve MPPT. MPPT technique was developed in [11] based on perturbing the voltage and the duty cycle. P&O algorithm based on the fuzzy logic controller was proposed in [12]. Current-sensorless MPPT technique was proposed in [13], where PV voltage is measured and cell temperature is estimated, and hence PV current could be calculated from a pre-specified look-up table [14]. However this technique suffers from complexity and reliability problem. Due to difficulties of ambient temperature estimation and model accuracy.

The technique proposed in [15] is able to omit one current sensor. PV current sensor, by estimating its value from the two measurable states  $V_{pv}$  (PV-voltage) and  $i_L$  (output-inductor-current). Although its effectiveness and ability to track the maximum power, special considerations are required for system stability.

In systems that require multivariable control, finite-set model predictive control (FS-MPC) becomes a very attractive solution [16]. Model predictive control (MPC) based MPPT has been presented in the literature [17], [18]. MPC based MPPT MPC based MPPT methodology presented in [18] is able to track the maximum power of PV-module efficiently at various environmental condition. However it uses three-sensors, two voltage-sensors and one current sensor.

In ideal cases, traditional boost converter is able to provide an infinite boosting ratio, but this is not the practical case [19], [20]. For achieving high step-up ability with high efficiency a lot of research has been done in the literature [21]–[23]. Cascaded boost converter [24] is a nonisolated DC/DC converter that is able to boost low input voltage to a higher level without using high duty cycle. On the other hand, a high rating switch is required due to its high voltage stress. Topologies with switched inductor and/or switched capacitor are good solution in providing high boosting ratio without the need for extreme duty cycle

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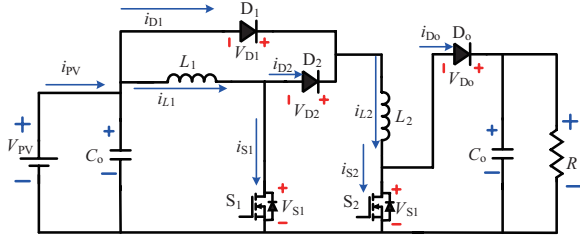


Fig. 1. New high step-up DC-DC converter.

[25], [26]. The voltage-lift technique is another modification to achieve high step-up capability. It has several advantages such as: low voltage stress on the active device, and high voltage gain. However, several diode-capacitor stages are required when the conversion ratio are very large, [27]–[29].

Switched inductor introduced in [30], is a transformerless topology able to generate a theoretical boosting ratio of  $((1+d)/(1-d))$ , however switch has voltage stress equal to output voltage and high voltage stress. Similarly, its diodes are under high voltage stress, which may deteriorate overall system efficiency. A similar idea but based on switched capacitor was presented in [31], where inductors were replaced by capacitors. The voltage gain in this case is  $(2/(1-d))$ . Hybrid switched inductor-capacitor was introduced to combine the advantages of switched inductor and switched capacitor configurations [32], [33]. Quadratic boost converter introduced in [34] provides higher step-up ration compared to switched inductor or switched capacitor, but with higher current stress on its power-switch. Switched inductor quadratic boost converter proposed in [18] is a combination of topologies exist in [26] and [34] are able to generate high step-up ratio, however the power-switches are under high voltage stress and current stress, which deteriorate its efficiency.

The contribution of this paper is as follow: ① Proposing and implementing a new high step-up transformerless DC-DC converter; ② Proposing a novel model predictive control maximum power powering tracking (MPC-MPPT) control algorithm with optimum number of sensors based on fixed and adaptive step-size. The proposed MPC-MPPT algorithm has advantage over the technique presented in [18] that output voltage sensor is omitted and replaced by a voltage observer to reduce the system cost, meanwhile it keeps the same better performance. Another feature for the developed MPC-MPPT algorithm that the step size could be set to be fixed or adaptive.

## II. NEW HIGH GAIN DC/DC CONVERTER

The configuration of the new high step-up DC/DC converter is illustrated in Fig. 1. It consists of two inductors, three diodes, one output capacitor and two power switches. In continuous conduction mode (CCM), the converter has two modes of operation depending on the switch status,  $S = \{0 \text{ or } 1\}$ . Upcoming paragraphs investigates the analysis of operation modes with taking into consideration the ideal characteristic of the circuit components. Operating waveforms of the converter is illustrated in Fig. 2. The two switches are triggered and turned off simultaneously. And hence, the two inductors are charging in operation mode and steady-state analysis, with assumption

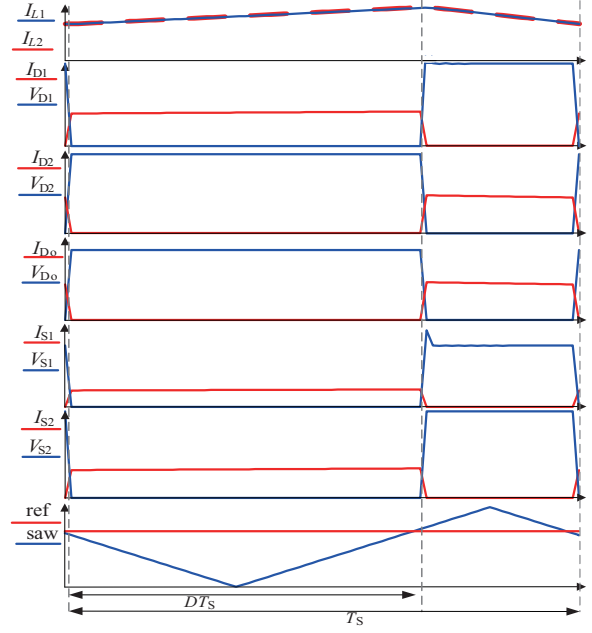


Fig. 2. New converter ideal key waveforms.

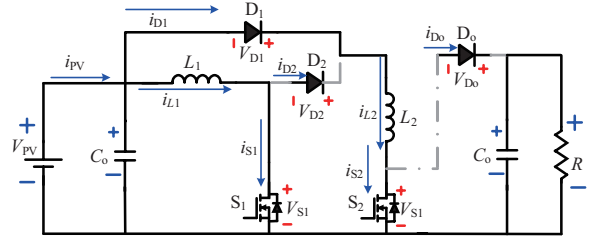


Fig. 3. Operation analysis of the proposed DC-DC converter, mode I.

of ideal parameters and inductors  $L_1$  and  $L_2$  are having the same inductance value, are as follows:

### A. Mode I

Mode I occurs as a result of triggering the two switches with high pulse signal, and hence they are turned on. As drawn in Fig. 3, once the switches are turned on, diodes  $D_2$  and  $D_0$  are turned off. And hence, inductors  $L_1$  and  $L_2$  are charging from the DC source. The characteristics equations of this mode are as follows:

$$\begin{cases} v_{L1}(t) = V_{PV} \\ i_{PV}(t) = C_{PV} \frac{dV_{PV}}{dt} + 2i_{L1} \\ v_{L2}(t) = V_{PV} \\ C_o \frac{dV_o}{dt} = i_{Co} = -V_o/R \end{cases} \quad (1)$$

### B. Mode II

Mode II occurs as a result of triggering the two switches with low pulse signal, and hence they are turned off. As seen from

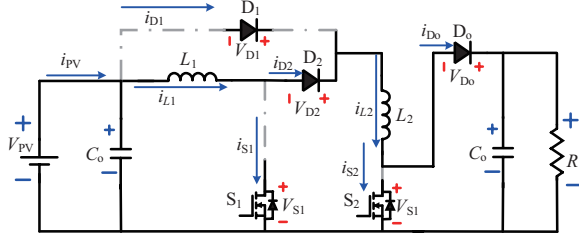


Fig. 4. Operation analysis of the proposed DC-DC converter, mode II.

Fig. 4, once the switches are turned off, diodes  $D_2$  and  $D_0$  are turned on to provide a path for the inductor currents and diode  $D_1$  is turned off. And hence, inductors  $L_1$  and  $L_2$  are discharging their energy in series to the output capacitor. The characteristic equations of this mode are as follows: parallel with the aid of diode  $D_1$  and discharging their energy in series to the load through diodes  $D_2$  and  $D_0$ . Analysis of modes I and II are repeated every switching cycle. Inductor volt-second balance and capacitor charge balance are the base criteria, we rely on to obtain input-output voltage relation in the upcoming analysis.

$$\begin{cases} v_{L1}(t) = v_{L2}(t) = \left( \frac{V_{PV} - V_o}{2} \right) \\ i_{PV}(t) = C_{PV} \frac{dV_{PV}}{dt} + i_{L1} \\ C_o \frac{dV_o}{dt} = i_{C_o} = i_{L1} - V_o/R \end{cases} \quad (2)$$

Applying inductor volt-second balance and capacitor charge-balance leads to

$$\begin{cases} \langle v_{L1}(t) \rangle = 0 \\ \langle v_{L2}(t) \rangle = 0 \\ \langle i_{C_o}(t) \rangle = 0 \end{cases} \quad (3)$$

The relation between output voltage, input voltage, and capacitors can be deduced from (1)–(3),

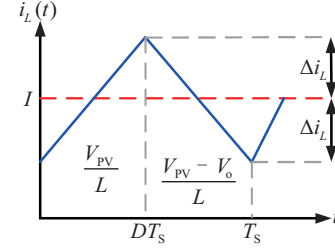
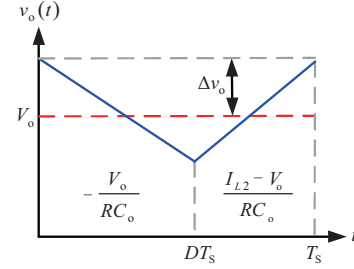
$$V_o = \left( \frac{1+D}{1-D} \right) V_{PV} \quad (4)$$

(4) is the voltage gain of the converter, where  $D$  is the duty cycle of the converter.

Circuit parameter design is based on the amount of ripple desired in the inductor current and capacitor voltages. In order to maintain CCM operation inductor current ripple is designed to be small. Design of each component of the circuit is given in the upcoming sections.

### C. Inductor $L_1$ and $L_2$ Design

The two inductors are symmetrical, with the same inductance value, and they carry the same current (see Fig. 2), one of

Fig. 5. Inductor  $L_1$  and  $L_2$  currents.Fig. 6. Capacitor  $C_o$  output voltage.

the inductor currents is sketched in Fig. 5. During the first subinterval, the change in inductor current,  $2\Delta i_L$ , is equal to the slope multiplied by the length of the subinterval value,

$$2\Delta i_L = \left( \frac{V_{PV}}{L} \right) DT_s \quad (5)$$

$$L = L_1 = L_2 = \left( \frac{V_{PV}}{2\Delta i_L} \right) DT_s \quad (6)$$

(6) is used to select inductor  $L_1$  and  $L_2$  value. Inductors value depend on input voltage  $V_{PV}$ , duty cycle  $D$ , sample time  $T_s$  and inductor current ripple  $\Delta i_L$ .

### D. Output Capacitor Design $C_o$

Likewise, the capacitor voltage waveform can be sketched and an expression derived for the output voltage ripple peak magnitude,  $\Delta v_o$ . The capacitor voltage waveform is sketched in Fig. 6. The change in the capacitor voltage,  $-2\Delta v_o$ , is equal to the slope multiplied by the length of subinterval:

$$\Delta v_o = \left( \frac{V_o}{2RC_o} \right) DT_s \quad (7)$$

$$C_o = \left( \frac{V_o}{2R\Delta v_o} \right) DT_s \quad (8)$$

(8) is used to select the output capacitor value. It is value depends on output voltage  $V_o$ , duty cycle  $D$ , sample time  $T_s$  and capacitor voltage ripple  $\Delta v_o$ .

A comparison between the proposed converter and other similar topologies exist in the literature in point of view of voltage gain and devices voltage stress is presented in Table I.

TABLE I  
COMPARISON OF STEP UP TOPOLOGIES

Topology		Boost	SL-boost	SC-boost	[35]	Interleaved	Quadratic	Proposed
No. switches		1	1	1	2	2	1	2
No. diode		1	4	3	3	3	3	3
Voltage gain		$\frac{1}{1-D}$	$\frac{1+D}{1-D}$	$\frac{1}{1-D}$	$\frac{3+D}{1-D}$	$\frac{1}{D(1-D)}$	$\left(\frac{1}{1-D}\right)^2$	$\frac{1+D}{1-D}$
Switch stress	S <sub>1</sub>	V <sub>o</sub>	V <sub>o</sub>	V <sub>o</sub> /2	(V <sub>o</sub> + V <sub>in</sub> )/4	V <sub>o</sub> /2	V <sub>o</sub>	(V <sub>o</sub> - V <sub>in</sub> )/2
	S <sub>2</sub>	N/A	N/A	N/A	(V <sub>o</sub> + V <sub>in</sub> )/4	D(1 - 2D)V <sub>o</sub>	N/A	V <sub>o</sub>
	S <sub>3</sub>	N/A	N/A	N/A	N/A	N/A	N/A	N/A
	S <sub>4</sub>	N/A	N/A	N/A	N/A	N/A	N/A	N/A
Diode stress	D <sub>1</sub>	N/A	V <sub>in</sub>	N/A	(V <sub>o</sub> /2) + V <sub>in</sub>	V <sub>o</sub> /2	V <sub>o</sub> (1 - D)	(V <sub>o</sub> + V <sub>in</sub> )/2
	D <sub>2</sub>	N/A	(V <sub>o</sub> - V <sub>in</sub> )/2	N/A	V <sub>o</sub> /2	V <sub>o</sub> /2	DV <sub>o</sub>	V <sub>in</sub>
	D <sub>3</sub>	N/A	(V <sub>o</sub> - V <sub>in</sub> )/2	N/A	N/A	N/A	N/A	N/A
	D <sub>o</sub>	V <sub>o</sub>	V <sub>o</sub>	(V <sub>o</sub> /2)	(V <sub>o</sub> + V <sub>in</sub> )/2	V <sub>o</sub> /2	V <sub>o</sub>	V <sub>o</sub>

### III. PROPOSED MPC BASED PV MPPT ALGORITHM

MPC presents a superior controller for plants, in the suggested application the plant represents the DC-DC converter, that require multiple state-variables needing to be controlled. MPC treats the plant as a finite set of linear models. Each model represents a switching state. MPC calculates the prediction of every switching state, then the minimization process occurs by the cost function, state that gives the minimum error is generated and applied to the plant [35],[36]. The suggested plant in PV system is the DC-DC converter, and due to continuous conduction operation, it has only two-state variables. Hence, a discrete-time model for the proposed DC/DC step-up converter is derived by applying forward Euler method to (1), switch on, and (2), switch off.

$$I_{PV}^1(k+1) = \left( \frac{2T_s}{L_1} \right) V_{PV}(k) + I_{PV}(k) \quad (9)$$

$$I_{PV}^0(k+1) = \left( \frac{T_s}{2L} \right) [V_{PV}(k) - V_o] + I_{PV}(k) \quad (10)$$

Where  $T_s$ ,  $L$ ,  $V_{PV}(k)$ ,  $I_{PV}(k)$ ,  $V_o$ ,  $I_{PV}^0(k+1)$ ,  $I_{PV}^1(k+1)$  are sampling time, inductance, voltage of PV, current of PV, output voltage, predicted PV current if the switch is turned off, and predicted PV current if the switch is turned on, respectively. A quick look to (9) and (10), it is clear that three sensors are required to predict the future value of the PV current. This would increase the system cost and size. (4) is a voltage observer, which is used to calculate the value of the output voltage with the available data of PV voltage and duty cycle. By this methodology, one voltage sensor is removed, which reduces the system cost and at the same time the system performance doesn't affected.

The final step in MPC control is the optimization of the possible future states, and select the optimum value that gives

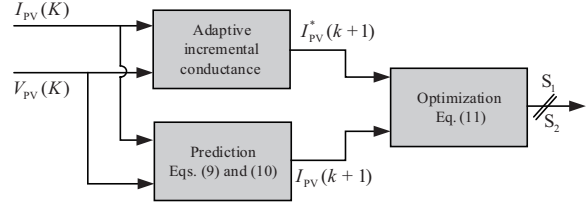


Fig. 7. Control block of the proposed control system.

the minimum error. Optimization is done by cost function:

$$g^{\sigma=\{0,1\}} = \left| I_{ref} - I_{PV}^{\sigma} \right|, \quad (11)$$

where  $g$ , and  $I_{ref}$  are the cost function and reference current respectively. Reference current  $I_{ref}$  is generated by incremental conductance algorithm. The proposed control block is depicted in Fig. 7 and MPC-MPPT algorithm is depicted in Fig. 8.

As illustrated in the figures, MPC-MPPT algorithm senses PV current and voltage only, then an incremental conductance approach is applied to generate the reference current that leads to maximum power operation. The MPC algorithm starts to regulate the converter to track the reference current. The capacitor voltage sensor is omitting by the voltage observer, described in the previous section. The perturbation step size, denoted as  $Z$ , in the flowchart can be set to fixed value to implement a fixed step size MPC-MPPT or it can be designed to be adaptive by the following formula:

$$Z = C \left| \frac{\Delta I}{\Delta D} \right|. \quad (12)$$

Where  $C$  is a scaling factor,  $\Delta I$  is the different between current and previous value of PV current, and  $\Delta D$  is the difference between current and previous value of the reference current.

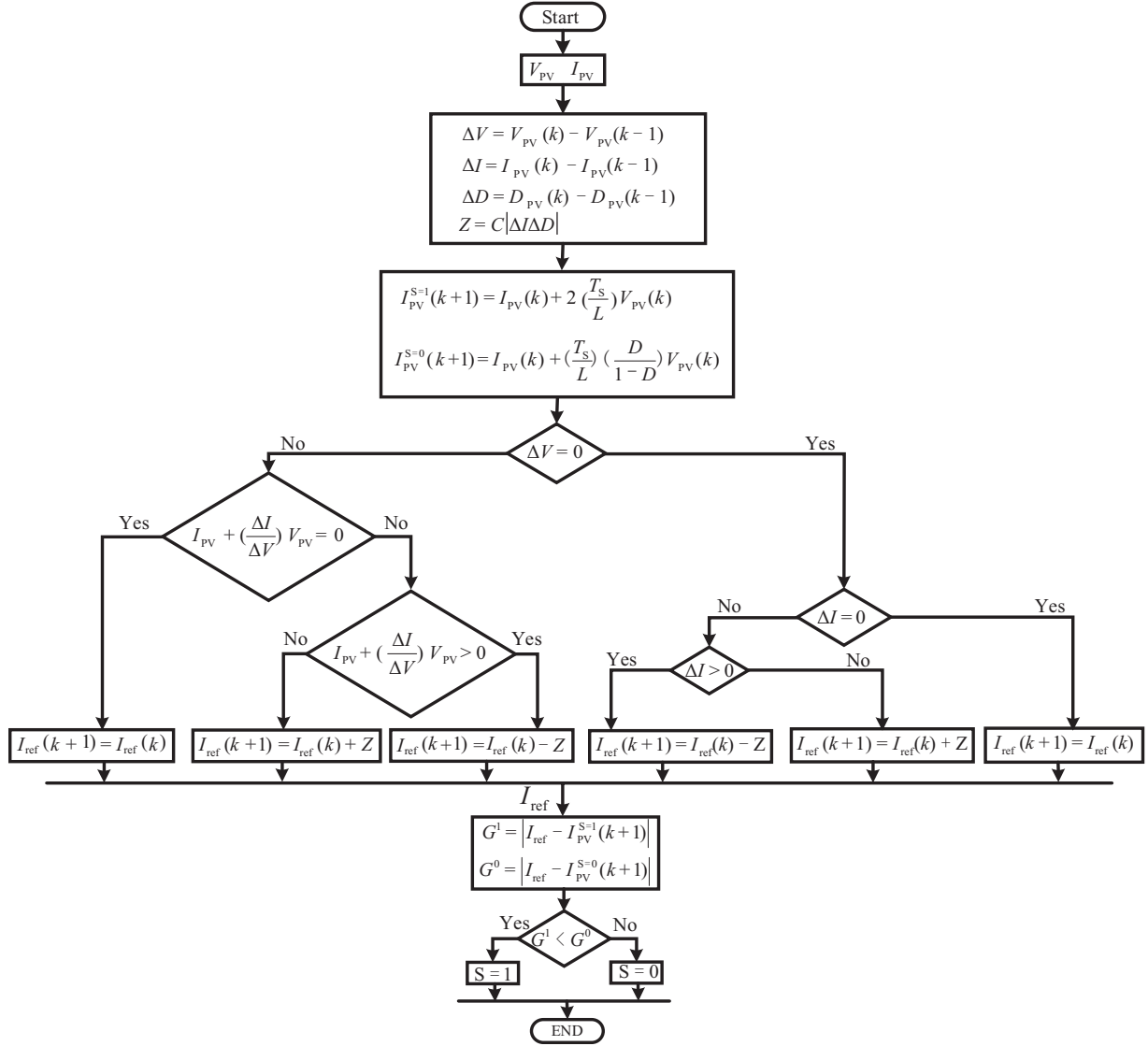


Fig. 8. Proposed MPC based MPPT with reduced number of sensors.

#### IV. RESULTS AND DISCUSSION

In order to verify and validate the decent operation of the proposed PV system, proposed DC/DC converter and its reduced sensor MPC-MPPT algorithm, the system is simulated using Simulink and full hardware set-up is demonstrated in laboratory. The developed prototype is depicted in Fig. 9. A 150 W DC/DC converter prototype is built using parameters in Table II. Dspace 1202 microbox controller is used to implement the proposed MPC-MPPT algorithm with sampling time  $T_s$  set to 15  $\mu$ s. LEM voltage and current sensors are used to measure voltages and currents. Chroma 62020H-150S PV simulator has been used to emulate PV module performance.

Converters assigned to integrate with PV module applications are required to achieve some distinct features such as high voltage gain and higher-efficiency. The proposed converter is designed to operate in CCM, and hence inductor currents

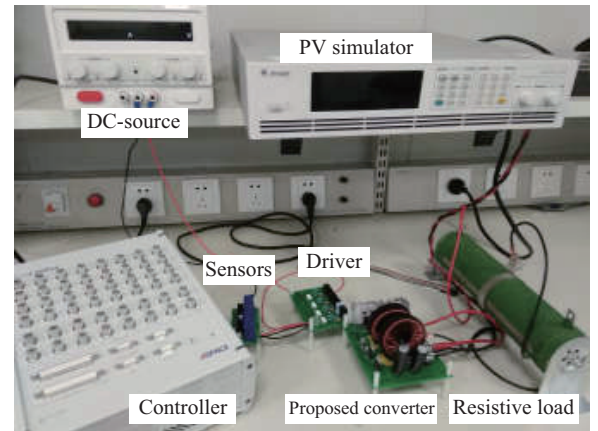


Fig. 9. Hardware prototype of the proposed system.

are always higher than zero, diode  $D_1$  is turned on and off in synchronization with the switches, and it has voltage stress less

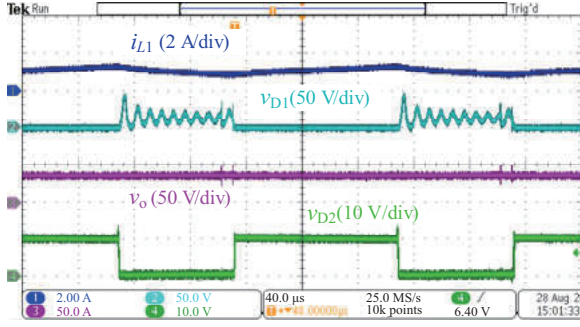


Fig. 10. Converter basic parameters waveforms: Ch1 inductor current, Ch2 diode D1 voltage, Ch3 output voltage and Ch4 Diode D2 voltage.

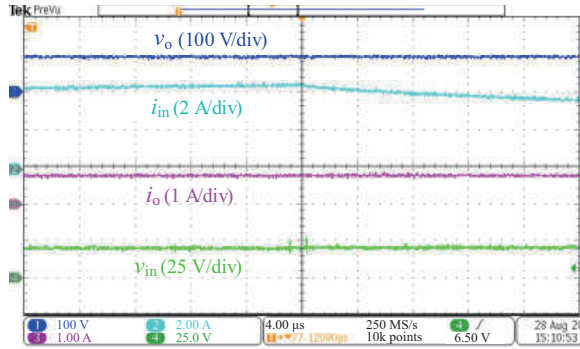


Fig. 11. Case study of step up ratio of 4 with duty cycle set to 0.65.

than half of the output voltage, while  $D_2$  and  $D_o$  conduct in a complementary manner, see Fig. 10. A boosting ratio check case of study is depicted in Fig. 11. In the case of the study, the input voltage is set to 20 V and duty cycle control to 0.65, the output voltage is measured around 100 V, step-up ratio, more than 4 times.

Efficiency is a very important factor for better selection and operation of any DC/DC converter. Proposed system efficiency is calculated analytically and measured experimentally. In both analytical calculation and measurement, parameters depicted in Table II are used. Measured efficiency of the proposed converter at different power level is depicted in Fig.12. The converter gives acceptable performance and measured efficiency around 92%. The proposed converter has similar component counts and voltage gain ability as topology presented in [26]. However, the maximum measured efficiency reported for the topology was found 90% and reduced with increase in boosting ratio to be less than 88% at 40 W. While the proposed configuration shows maximum measured efficiency 94% and reduced to 91% at 100 W under 4 boosting ratio. Quadratic boost converter is able to provide higher boosting ratio than the proposed converter in this paper, however reported efficiency of quadratic boost converter is round 80% [37] and its switch current stress is very high compared to the converter proposed in this paper.

Efficient maximum power point tracking is an important factor to make use of photovoltaic generation. The more

TABLE II  
HARDWARE PROTOTOTYPE PARAMETERS

Components	Parameters
PV panel voltage ( $V_{PV}$ )	18–40 V
Sampling time	15 $\mu$ s
Circuit inductors ( $L_1/L_2$ )	3 mH
Input capacitor ( $C_1$ )	260 $\mu$ F
Output capacitor ( $C_2$ )	260 $\mu$ F
Power MOSFETs ( $S_{1-2}$ )	IRFP264
Power diode ( $D_{1-2}$ )	BYV72EW-200
Output diode ( $D_o$ )	BYV72EW-200

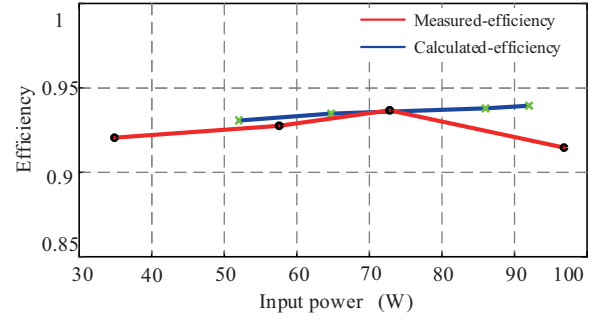


Fig. 12. Converter measured efficiency vs input power under  $V_{in} = 20$  V, and  $M = 4$ .

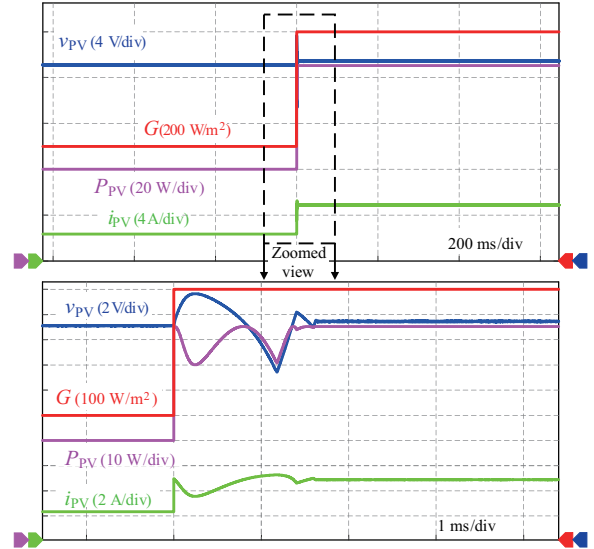


Fig. 13. PV output voltage, current and power with gradual change in solar irradiation.

efficient the MPPT tracker, the more economic operation of the photovoltaic system. The proposed MPC-MPPT algorithm is designed in this paper to operate with a fixed and adaptive step size. The system is studied under normal and weather changing conditions. As has been discussed earlier in this paper, the output characteristic of the PV panels is affected mainly by solar radiation and ambient temperature. But, most influencer is solar radiation. In Fig. 13, an abrupt change in solar radiation is simulated with a change in the radiation( $W/m^2$ ) from 500 to 1000. The controller is tracking the maximum power under

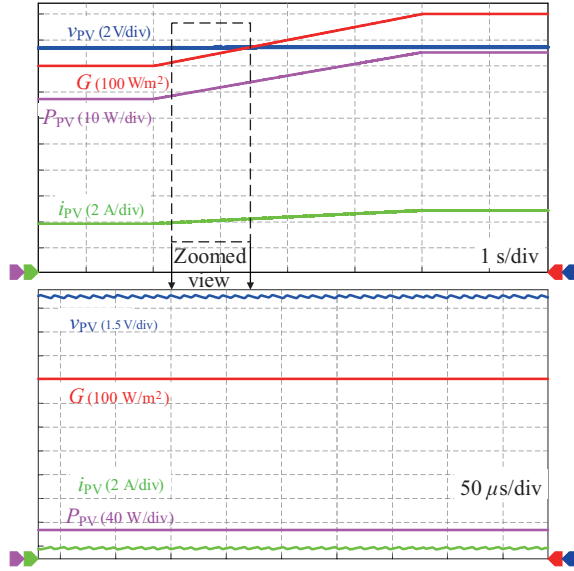


Fig. 14. PV output voltage, current and power with an abrupt change in solar irradiation.

all condition with efficiency more than 99.5%. In most of the real cases, the change in solar radiation is not abrupt, it has a gradual change, for example, a moving cloud or accumulated sand. To take this point into account, a gradual change in solar radiation is simulated in Fig. 14. The controller is acting in a very superior way and able to track the maximum power in very high-efficiency way.

In Fig. 15, PV simulator is programmed to generate PV curve with the following characteristic ( $P_{\max} = 85$  W,  $V_{\max} = 25$  V and  $I_{\max} = 3.4$  A). As the graph illustrates, the algorithm successfully tracks the maximum power. In Fig. 16, where an abrupt change in solar radiation has been taken place. Maximum output power changed according to radiation change from 50 W to 85 W, then back again to 50 W. The algorithm successfully tracks the maximum power at all conditions as illustrated in the figure.

Figs. 17 and 18 present a detailed comparison between the proposed MPC-MPC with fixed step size, proposed MPC-MPPT with adaptive step size and the well-known IncCond algorithm at different step size. The comparison includes an abrupt change in solar radiation and the gradual change in solar radiation. Fig. 17 represents the comparison between the MPPT methodologies under an abrupt change in radiation, while Fig. 18 demonstrates the comparison among the different techniques at the gradual change in solar radiation. IncCond algorithm is one of the widest algorithms used in PV applications, its operation depends on that the slope of PV curve is zero at the maximum power. Two perturbation step size is selected for the IncCond, 0.05 and 0.05. In Fig. 17, with an abrupt change in solar irradiation ( $\text{W/m}^2$ ), radiation changed from 1000 to 800, the MPPT is tracked by all the MPPT methodologies.

The proposed system performance is almost doesn't change

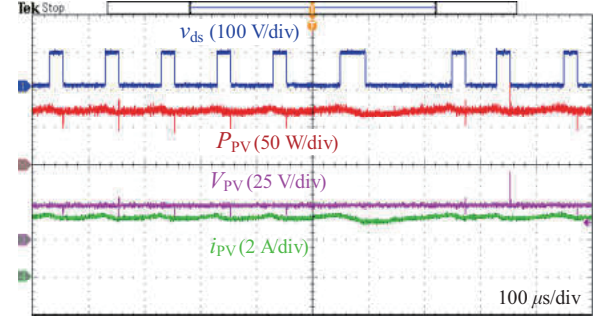


Fig. 15. Case study where PV curve characteristics ( $V_{\max} = 25$  V, and  $I_{\max} = 3$  A).

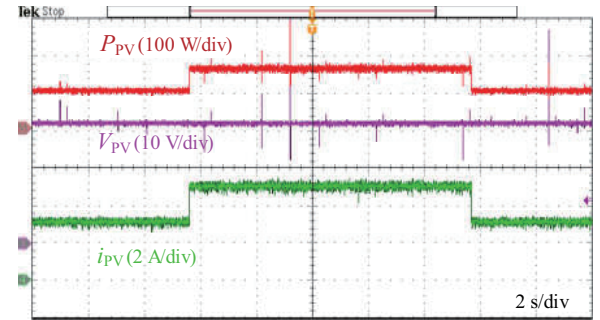


Fig. 16. Abrupt change in solar radiation case of study  $P_{\max}$  change from 50 W to 85 W and then back to 50 W again.

with an adaptive or fixed step size. It is able to track the maximum power under all condition with efficiency more than 99.5%. While the performance of the IncCond, greatly affected by the step size. IncCond has big oscillation with step size 0.02 than 0.05 and its efficiency becomes lower. In big systems, this reduction in efficiency may reduce the revenue of the system and the feasibility of the system. In gradually changed solar radiation depicted in Fig. 18. The proposed system has superior performance, almost the same performance with fixed or adaptive step size, while IncCond algorithm depends mainly on the step size.

Step size 0.02 has large ripple and lower efficiency than step size of 0.05. The main important factors that may affect the selection of one MPPT technique over another one are the complexity of the technique, its efficiency, tracking capability, tracking speed and reliability. A comparison between the proposed methodology and other techniques are depicted in Table III. The proposed system has superior performance such as high efficiency, fastly tracking the maximum power, and reliable technique.

## V. CONCLUSION

The contribution of this paper could be summarized as follows: ① Proposing and implementing a new high gain DC-DC converter for PV applications; ② Developing and implementing MPPT-MPC algorithm with optimum number of sensors for PV applications. The developed DC-DC converter

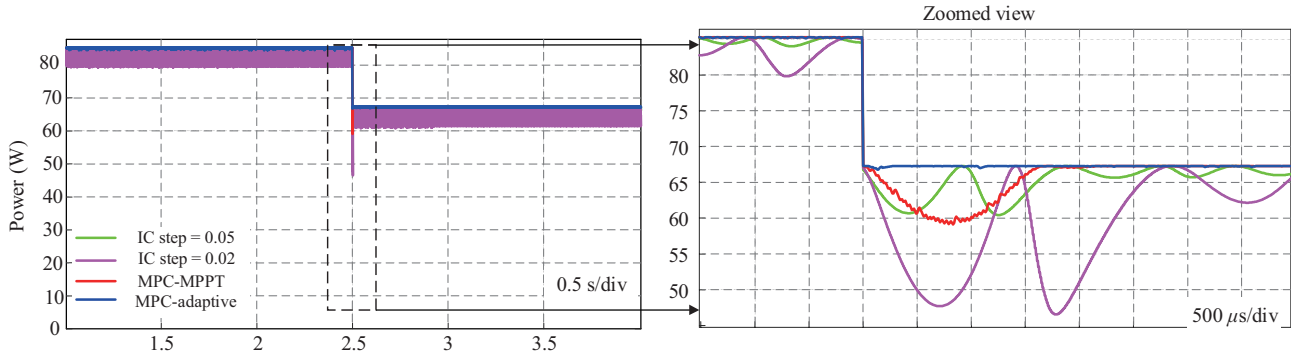


Fig. 17. Comparison between proposed and other MPPT techniques under an abrupt radiation change.

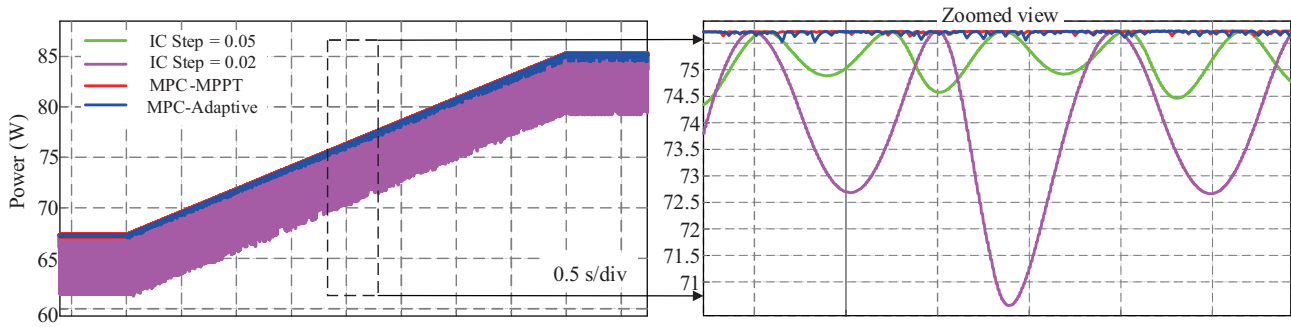


Fig. 18. Comparison among the different techniques at the gradual change in solar radiation.

TABLE III  
COMPARISON AMONG DIFFERENT MPPT METHODOLOGY

Evaluated parameters	IncCond	[38]	PSO[39]	Fractional Isc	Fractional Voc	Hill climbing	Fuzzy logic	Neural network	Proposed
Complexity	Simple	Moderate	Moderate	Moderate	Simple	Simple	High	High	Moderate
Efficiency	Low	High	High	Low	Low	Low	Moderate	Moderate	High
Tracking capability	Moderate	Moderate	Moderate	Moderate	Moderate	Moderate	High	High	High
Tracking speed	High	Moderate	Moderate	Moderate	Moderate	Moderate	High	High	High
Steady-state oscillation	Yes	No	No	Yes	Yes	Yes	No	No	No
Reliability	Moderate	Moderate	High	Low	Low	Low	Moderate	Moderate	High
Response to load variation	Moderate	Moderate	Moderate	Low	Low	Moderate	Moderate	Moderate	Fast

was having superior advantaged over its transformerless counterparts as demonstrated through the manuscript. The developed MPC-MPPT algorithm, output voltage sensor has replaced by an observer to optimize the system cost and at the same time preserve the same performance. Another advantage of the developed algorithm that it could work with fixed and adaptive step-size. Although the superior performance of the proposed MPC-MPPT algorithm, working with variable switching frequency uneased the optimum design of the power converter. Both simulation and experimental results are consistence and agreed with the analytical analysis.

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